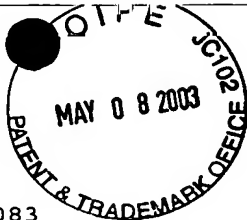


Rev. 1/03

Modified PTO 1083
For Other Than A Small Entity



Response Under 37 CFR 1.110
Expedited Procedure
Examining Group 2819

PATENTS

AF/2800

Attorney Docket No. AL/79Re

Applicant(s) : Nghia Tran, Ying Xuan Li, Janusz Balicki and John Costello

Application No.: 10/084,757

Confirmation No.: 4411

Filed : October 19, 2001

For : SYSTEM FOR COUPLING PROGRAMMABLE LOGIC
DEVICE TO EXTERNAL CIRCUITRY WHICH SELECTS
A LOGIC STANDARD AND USES BUFFERS TO MODIFY
OUTPUT AND INPUT SIGNALS ACCORDINGLY

Group Art Unit : 2819

Examiner : Daniel D. Chang

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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TRANSMITTAL LETTER

Sir:

Transmitted herewith: ☐ a Preliminary Amendment;
☒ a Response to Examiner's Action; ☐ a Supplemental Amendment;
☐ a substitute Specification; ☐ a Declaration; ☐ a Supplemental Declaration; ☐ a
Power of Attorney; ☐ an Associate Power of Attorney; ☐ formal drawings; to be
filed in the above-identified patent application.

FEE FOR ADDITIONAL CLAIMS

☒ A fee for additional claims is not required.

☐ A fee for additional claims is required.

The additional fee has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA		RATE		ADDITIONAL
TOTAL CLAIMS	41	-	41*	=	0	x	\$18	=	\$.00
INDEPENDENT CLAIMS	7	-	7**	=	0	x	\$84	=	\$.00
FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM						+	\$280	=	\$.00

* If less than 20, insert 20.

** If less than 3, insert 3.

TOTAL \$.00

- ☐ A check in the amount of \$ _____ in payment of the fee for additional claims is transmitted herewith.
- ☒ The Director is hereby authorized to charge payment of any additional fees required under 37 C.F.R. § 1.16 in connection with the paper(s) transmitted herewith, or credit any overpayment of same, to Deposit Account No. 06-1075. A duplicate copy of this transmittal letter is transmitted herewith.
- ☐ Please charge \$ _____ to Deposit Account No. 06-1075 in payment of the fee for additional claims. A duplicate copy of this transmittal letter is transmitted herewith.

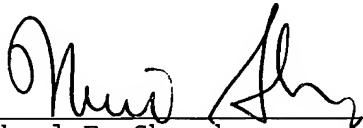
EXTENSION FEE

- ☐ The following extension fee is applicable to the Response filed herewith:
☐ \$110.00 extension fee for response within first month pursuant to 37 C.F.R. § 1.136(a); ☐ \$410.00 extension fee for response within second month pursuant to 37 C.F.R. § 1.136(a); ☐ \$930.00 extension fee for response within third month pursuant to 37 C.F.R. § 1.136(a); ☐ \$1,450.00 extension fee for response within fourth month pursuant to 37 C.F.R. § 1.136(a); ☐ \$1,970.00 extension fee for response within fifth month pursuant to 37 C.F.R. § 1.136(a).
- ☐ A check in the amount of ☐ \$110.00 ☐ \$410.00 ☐ \$930.00 ☐ \$1,450.00 ☐ \$1,970.00 in payment of the extension fee is transmitted herewith.
- ☒ The Director is hereby authorized to charge payment of any additional extension fees required under 37 C.F.R. § 1.17 in connection with the paper(s) transmitted herewith, or credit any overpayment of same, to Deposit Account No. 06-1075. A duplicate copy of this transmittal letter is transmitted herewith.
- ☐ Please charge the ☐ \$110.00 ☐ \$410.00 ☐ \$930.00 ☐ \$1,450.00 ☐ \$1,970.00 extension fee to Deposit Account No. 06-1075. A duplicate copy of this transmittal letter is transmitted herewith.

I hereby certify that this
Correspondence is being
deposited with the U.S.
Postal Service as First
Class Mail in an envelope
Addressed to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450 on

May 5 2022
Claire J. Saint-Yves Goodman

Signature
Signature of Person Signing


Michael E. Shanahan
Registration No. 43,914
Attorney for Applicant(s)
Fish & Neave
Customer No. 1473
1251 Avenue of the Americas
New York, New York 10020-1105
Tel.: (212) 596-9000



Response Under 37 CFR 1.116
Expedited Procedure
Examining Group 2819

#10/ Response
PATENTS
174707986
5/13/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Nghia Tran, Ying Xuan Li, Janusz Balicki and John Costello

Application No.: 10/084,757 ✓

Filed : October 19, 2001

For : SYSTEM FOR COUPLING PROGRAMMABLE LOGIC DEVICE TO EXTERNAL CIRCUITRY WHICH SELECTS A LOGIC STANDARD AND USES BUFFERS TO MODIFY OUTPUT AND INPUT SIGNALS ACCORDINGLY

Group Art Unit : 2819

Examiner : Daniel D. Chang

New York, New York 10020
May 5, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION

Sir:

In response to the Office Action dated March 31, 2003,
please consider the following remarks:

Okay to Enter
Specs/03